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REPUBLIC VAN SUID-AFRIKA

DEPARTMENT OF TRADE AND
INDUSTRY

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PATENT OFFICE

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REC'D 09 OCT 2003

WIPO

PCT

the documents attached hereto, are true copies of Application
Form P1 and P2, Provisional Specification and Drawings of Patent
Application No. 2002/5873 filed in the name of **SOUTH AFRICAN
MICRO ELECTRONIC SYSTEMS (PROPRIETARY) LIMITED**
on the 23 July 2002 and the invention entitled: "**VOLTAGE
MEASUREMENT**".

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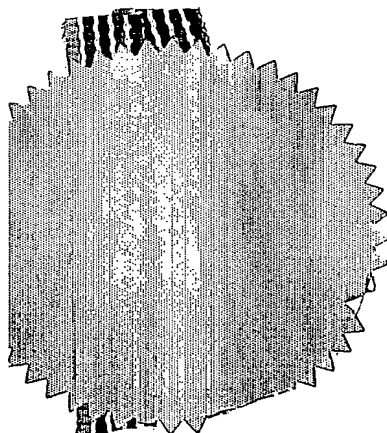
PRETORIA

in die Republiek van Suid-Afrika, hierdie
in the Republic of South Africa, this

16th

dag van
day of

September 2003



Registrateur van Patente

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REPUBLIC OF SOUTH AFRICA
PATENTS ACT, 1978

REPUBLIC OF SOUTH AFRICA
REVENUE

Form P.1

APPLICATION FOR A PATENT AND ACKNOWLEDGEMENT OF RECEIPT
(Section 30 (1) - Regulation 22)

The grant of a patent is hereby requested by the undermentioned applicant on the basis of the present application filed in duplicate.

23 7 02 R 06090

INKOMSTE
REPUBLIEK VAN SUID AFRIKA

AGENT'S REFERENCE

P15215ZA00

OFFICIAL APPLICATION NO

21 01 22002/5873

FULL NAME(S) OF APPLICANT(S)

SOUTH AFRICAN MICRO ELECTRONIC SYSTEMS (PROPRIETARY) LIMITED

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TITLE OF INVENTION

VOLTAGE MEASUREMENT

54

THE APPLICANT CLAIMS PRIORITY AS SET OUT ON THE ACCOMPANYING FORM P2
The earliest priority claimed is

THIS APPLICATION IS FOR A PATENT OF
ADDITION TO PATENT APPLICATION NO.

21

01

THIS APPLICATION IS FRESH APPLICATION IN TERMS
OF SECTION 37 AND BASED ON APPLICATION NO.

21

01

THIS APPLICATION IS ACCOMPANIED BY :

X	1a	A single copy of a provisional specification of 10 pages.
	1b	Two copies of a complete specification of pages.
X	2a	Informal drawings of 4 sheets.
	2b	Formal drawings of sheets.
	3	Publication particulars and abstract (form P8 in duplicate).
	4	A copy of figure of the drawings for the abstract.
	5	Assignment of invention (from the inventors) or other evidence of title.
	6	Certified priority document(s).
	7	Translation of priority document(s).
	8	Assignment of priority rights.
	9	A copy of form P2 and a specification of S.A. Patent Application.
X	10	A declaration and power of attorney on form P3.
	11	Request for ante-dating on form P4.
	12	Request for classification on form P9.
	13a	Request for delay of acceptance on form P4.
	13b	

21

01

DATED 23 July 2002

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74

Patent Attorney for Applicant(s)

RECEIVED

OFFICIAL DATE STAMP

REGISTRAR OF PATENTS

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REPUBLIC OF SOUTH AFRICA

REGISTER OF PATENTS

PATENTS ACT, 1978

OFFICIAL APPLICATION NO.

LODGING DATE : PROVISIONAL

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21 01 2002 5873 22 23 July 2002

43

INTERNATIONAL CLASSIFICATION

LODGING DATE : COMPLETE

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51 23

FULL NAME(S) OF APPLICANT(S) / PATENTEE(S)

71 SOUTH AFRICAN MICRO ELECTRONIC SYSTEMS (PROPRIETARY) LIMITED

APPLICANTS SUBSTITUTED :

71

DATE REGISTERED

ASSIGNEE(S)

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DATE REGISTERED

FULL NAME(S) OF INVENTOR(S)

72 SUNTKEN, Artur Wilhelm

PRIORITY CLAIMED

COUNTRY

NUMBER

DATE

N.B. Use international
abbreviation for country.
(See Schedule 4)

33

31

32

TITLE OF INVENTION

54 VOLTAGE MEASUREMENT

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REF

P15215ZA00

PATENT OF ADDITION NO.

DATE OF ANY CHANGE

61

FRESH APPLICATION BASED ON

DATE OF ANY CHANGE

REPUBLIC OF SOUTH AFRICA

PATENTS ACT, 1978

PROVISIONAL SPECIFICATION
(Section 30 (1) - Regulation 27)

OFFICIAL APPLICATION NO.		LODGING DATE	AGENT'S REFERENCE
21	01 54092/337 22	23 July 2002	P15215ZA00

FULL NAME(S) OF APPLICANT(S)	
71	SOUTH AFRICAN MICRO ELECTRONIC SYSTEMS (PROPRIETARY) LIMITED

FULL NAME(S) OF INVENTOR(S)	
72	SUNTKEN, Artur Wilhelm

TITLE OF INVENTION	
54	VOLTAGE MEASUREMENT

- However, the use of bridge rectifier circuits may introduce errors in the measurement of the AC input voltage to the bridge rectifier. This is particularly important where the power supply is used to provide power for an electricity consumption meter, which is, in turn, used to measure the input line voltage to the power supply, i.e. the voltage between the live and neutral conductors of an AC electricity reticulation circuit. Voltage errors in the voltage measurements of such meters originate largely as a result of the fact that there is no stable reference voltage, with respect to which the live voltage to be measured may be referenced, since the meter generally has a floating earth providing a reference for voltage measurements. The error introduced in the measurement due to the bridge rectifier circuit generally takes the form of a small error in phase and amplitude and is time dependant. Furthermore, the size of the error is somewhat unpredictable and depends partly on the DC voltage level of the bridge output.

OBJECT OF THE INVENTION

It is an object of this invention to provide a method and means for ascertaining voltage for use, particularly, but not exclusively, in the metering of electrical power, which will, at least partially, alleviate some of the abovementioned problems.

SUMMARY OF THE INVENTION

According to a first aspect of the invention there is provided, in an electronic device having an electrical circuit connected to live and neutral lines of an AC power supply via a bridge rectifier for the provision of DC power to the circuit, a method for ascertaining the AC power supply voltage, the method including providing a corrected voltage signal comprising a differential between signals representative of

the live and neutral AC voltage signals, each signal referenced to a preselected common voltage reference point in the circuit of the device.

The reference point may be a floating earth point of the circuit of the device and the method may include the steps of

- providing a first neutral error voltage signal representative of the voltage signal of the neutral line of the AC power supply with reference to the floating earth of the circuit;

- providing a second live error voltage signal representative of the voltage signal of the live line of the AC power supply with reference to the floating earth of the circuit; and

- subtracting the first error voltage signal from the second error voltage signal to provide the corrected voltage signal.

Instead of subtracting the first error voltage signal from the second error voltage signal to provide the corrected voltage signal, the method may include the steps of

- inverting the first error voltage signal; and

- adding the inverted signal to the second error voltage signal to provide the corrected voltage signal.

The step of inverting the first error voltage signal may be by means of an inverter. The inverter may comprise an amplifier with an amplification of minus one. The bridge rectifier may have a bridge rectifier circuit having first and second input nodes for connection respectively to live and neutral lines of an AC power supply and may have positive and negative DC output nodes for connection to a load. In one embodiment of the invention, the inverter is operable to invert the voltage signal measured between a neutral input node of the rectifier bridge circuit and the floating earth point of the device driven by the power supply (the neutral error voltage) and to add the inverted signal to the voltage signal measured between the live AC line and the floating earth of the device (the live error voltage).

VOLTAGE MEASUREMENT

FIELD OF THE INVENTION

This invention relates to electronics. More particularly, the invention relates to the ascertaining of voltage. In particular, the invention relates to a method and a means for ascertaining voltage. In a specific embodiment, the invention relates to a power supply and to a method for compensating for an error in a power supply, more particularly a bridge power supply. The invention has particular application in the field of metering.

BACKGROUND TO THE INVENTION

The use of bridge rectifier circuits in power supplies is well known. A common full wave bridge rectifier power supply has a quartet of diodes arranged in circuit to provide a rectifier stage of the power supply. The use of a bridge rectifier circuit provides a full wave rectifier, which does not generally require the use of a transformer. The bridge rectifier further permits the size of a voltage dropping capacitor, which is commonly used in the input stage of such power supplies, to be reduced to about half the capacitance of a corresponding capacitor in a non-bridge rectifier circuit, thereby reducing power consumption in the power supply circuit.

In one embodiment of the invention, the device is an electricity consumption meter (power meter), which may be a current transformer meter.

According to a second aspect of the invention there is provided, in an electronic device having an electrical circuit connected to live and neutral lines of an AC power supply via a bridge rectifier for the provision of DC power to the circuit, a voltage ascertaining means operable to provide a voltage signal comprising a differential between voltage signals representative of the live and neutral AC voltage signals, each signal referenced to a preselected common voltage reference point in the circuit of the device.

The bridge rectifier may include a bridge rectifier circuit having first and second input nodes for connection respectively to live and neutral lines of an AC power supply and having positive and negative DC output nodes for connection to a load.

The voltage ascertaining means may include an error compensation circuit operably connected to the bridge rectifier circuit. The error compensation circuit may comprise an inverter, which may be an amplifier having an amplification of minus one. In one embodiment of the invention, the inverter is operable to invert the voltage signal measured between a neutral input node of the rectifier bridge circuit and the floating earth point of the device driven by the power supply (the neutral error voltage) and to add the inverted signal to the voltage signal measured between the live AC line and the floating earth of the device (the live error voltage).

In an embodiment of the invention, the error compensation circuit comprises part of a power supply for providing power to an energy meter for the measurement of power use from an AC power source, such as that provided by a utility provider. Then, the energy meter provides the load for the power supply and may be connected to the output nodes of the bridge rectifier circuit via an intermediate stage of the power supply circuit. Such an energy meter may have a floating earth providing a reference

for measuring the voltage of the AC signal to be metered. The error compensation circuit may compensate for an error in the AC voltage signal measured by the meter, by adding an inverted error compensating signal to the live line from the AC power supply. It will be appreciated that this will result in the elimination or reduction of the error in the voltage signal measured by the meter between its floating earth and the live line of the AC power supply. It will further be appreciated that, instead of the addition of an inverted error compensating signal, the error compensation circuit may provide means for subtracting the error compensating signal from the voltage signal measured by the meter between its floating earth and the live line of the AC power supply. Those familiar with power metering will appreciate that the voltage in the measurement circuit may be stepped down from the AC input voltage, so that the signal measured by the meter bears a known relationship to, and is representative of, the AC live voltage. Similarly, the error compensating voltage may be stepped down correspondingly.

The invention extends to a bridge power supply incorporating a voltage ascertaining means as hereinbefore described.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention is now described below, by way of example only, and with reference to the accompanying drawings in which:

Figure 1 shows a block diagram illustrating a method for ascertaining an AC voltage, in accordance with the invention;

Figure 2 shows a block diagram of an alternative embodiment of the method of the invention;

Figure 3 shows a circuit diagram of a general form of a DC bridge power supply in accordance with the invention;

Figure 4 shows a circuit diagram of an embodiment of the DC bridge power supply; and

Figure 5 shows a series of voltage signal traces measured at various points of the power supply of Figure 4.

DETAILED DESCRIPTION OF THE INVENTION

In the drawings, reference numeral 1 generally refers to a voltage ascertaining means, in accordance with an aspect of the invention.

In Figures 1 and 2, the form of the circuit of the voltage ascertaining means is not shown. In Figure 1, the means 1 is operable to subtract an error voltage signal from a voltage signal containing a voltage error to provide a corrected voltage signal for use or measurement by an unspecified device. In Figure 2, the voltage error signal is inverted by means of an amplifier 2 having an amplification of minus one and the inverted error signal added to the voltage signal containing the voltage error, to provide the corrected voltage signal.

In Figures 3 and 4, reference numeral 10 generally refers to a circuit for a DC bridge power supply in accordance with the invention.

The power supply circuit 10 is shown connected to a power meter 12 (details of which are omitted) for metering electricity consumption from an electricity reticulation circuit having live and neutral lines 14 and 16, respectively.

The power supply circuit 10 has a full wave bridge rectifier circuit 18. The bridge rectifier circuit 18 has first and second input nodes 20,22, which are connected respectively to the live and neutral input lines 14,16 of the AC power supply (the reticulation circuit). Further, the bridge rectifier circuit 18 has positive and negative output nodes 24 and 26 respectively, which are connected to a load, being generally the power meter 12 driven by the DC bridge power supply 10, via an intermediate stage of the power supply circuit 10.

The power supply circuit 10 has an error compensation circuit 30 operatively connected to the second input node 22 of the bridge rectifier circuit 18. The error compensation circuit 30 forms part of a voltage ascertaining circuit 31, being an embodiment of the voltage ascertaining means of the invention. The circuit 31 provides a voltage signal, at 36, comprising a differential between voltage signals representative of the live and neutral AC voltage signals of the lines 14,16. Each of the voltage signals is referenced to a preselected common voltage reference point in the circuit of the device, in this case the floating earth point 34 of the power meter 12.

The error compensation circuit 30 detects the so called neutral error by detecting the voltage signal measured between the neutral input node 22 of the bridge rectifier circuit 18 and a floating earth 34 of the power meter 12. The compensating error correction signal is provided by inverting the detected error signal. It will be appreciated that the power meter 12 has a floating earth 34 providing a reference for measuring the voltage of the AC signal to be metered.

The error compensation circuit 30 comprises an inverter, which may be an amplifier having an amplification of minus one, as shown in the embodiment shown in Figure 4. Then, it will be appreciated that the addition of the compensating error correction signal to the live line 32 from the AC power supply will result in the elimination or reduction of the error in the voltage signal measured by the meter 12 between its floating earth 34 and the live line of the AC power supply (measured in a branch 36

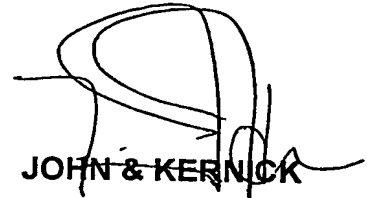
of the circuit 10 after the introduction of the compensating voltage). It will be further appreciated that both the AC voltage being measured and the compensating error correction signal are stepped down by the use of suitable resistors in the circuit 31. In an alternative embodiment of the invention, the error compensation circuit 30 may be replaced by a suitable device for subtracting the neutral voltage error directly from the AC voltage to be measured, obviating the need for an inverter.

Figure 5 shows various voltage signal traces with reference to the power supply circuit 10 of Figure 4. In Figure 5, a trace of the voltage signal between the floating earth point 34 of the meter 12 and the live line of the AC power supply before correction, as at 32 (live line of the AC power supply), is indicated by reference letter "A" in Figure 5. The trace is basically sinusoidal with a superimposed error having the effect of a small amplitude and phase shift in the sinusoidal wave form. The error is measured between the floating earth point 34 of the meter 12 and the neutral input node 22 of the rectifier bridge circuit 18 (neutral line 16 of the AC power supply). The wave form of the neutral error voltage is a time-dependant wave which is approximately 90° out of phase with the voltage signal A and is indicated by reference letter "B" in Figure 5. Finally, the trace indicated by reference letter "C" in Figure 5, shows the voltage wave form resulting from the inversion of the error voltage signal B and the addition thereof to the wave form A. The resultant voltage signal C is generally sinusoidal in form and has an amplitude approximately equal to that of the AC signal of the reticulation circuit (after taking account of voltage drops through relevant voltage reducing resistors in the circuit 10).

By means of the invention there is provided method and means for ascertaining a voltage, and a power supply which makes use of a DC rectifier bridge and corrects for errors or distortions introduced by the rectifier bridge. This permits the accurate metering of electricity usage by means of a power meter supplied with DC power by the power supply. The meter may more accurately meter the input AC supply voltage,

notwithstanding that it uses as a reference its own floating earth for the purposes of voltage measurement.

DATED THIS 23RD DAY OF JULY 2002



JOHN & KERNICK
FOR THE APPLICANT

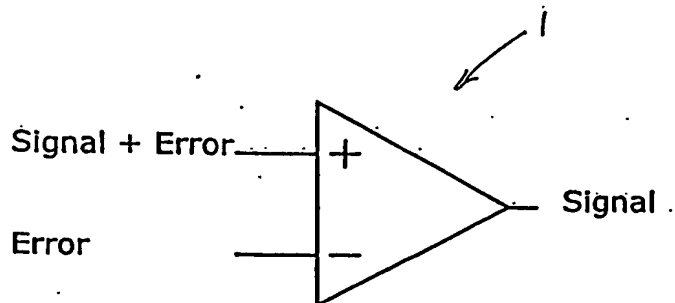


Fig 1

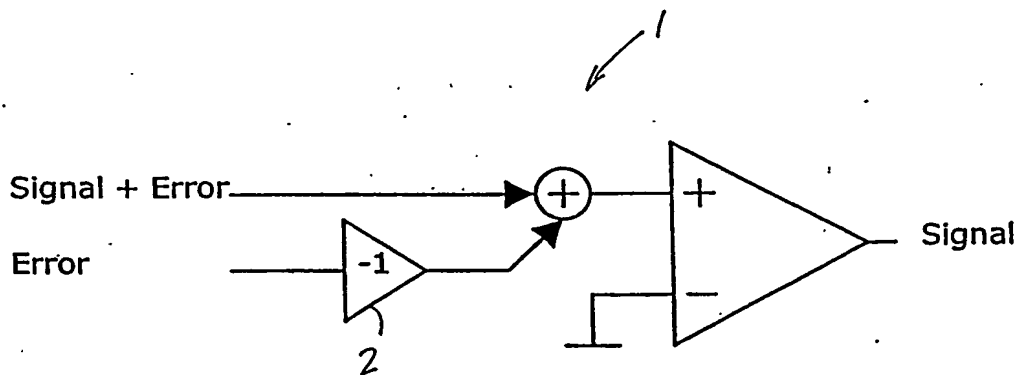
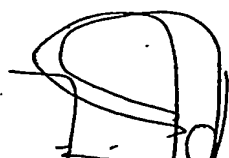


Fig 2


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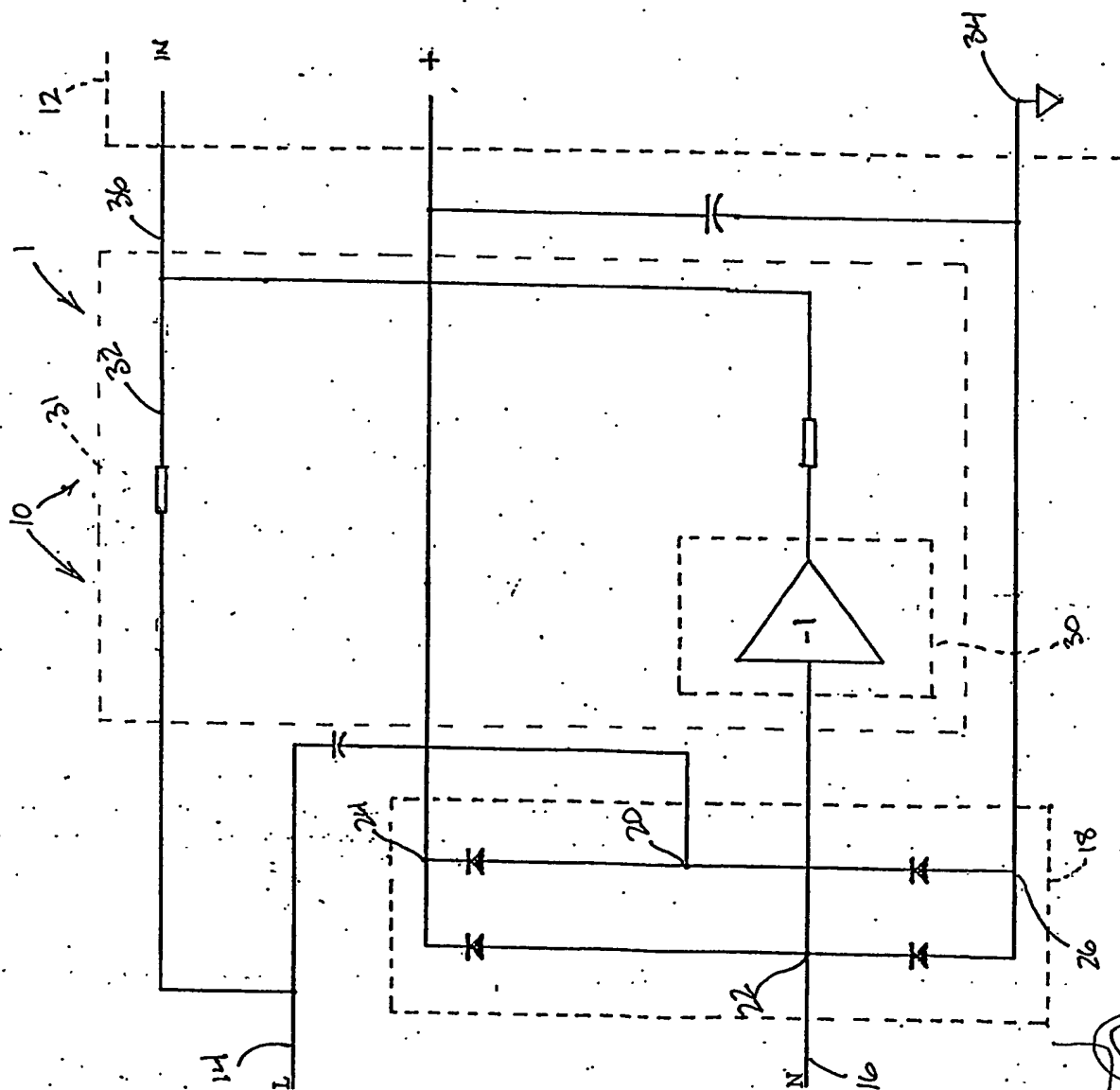


Fig 3

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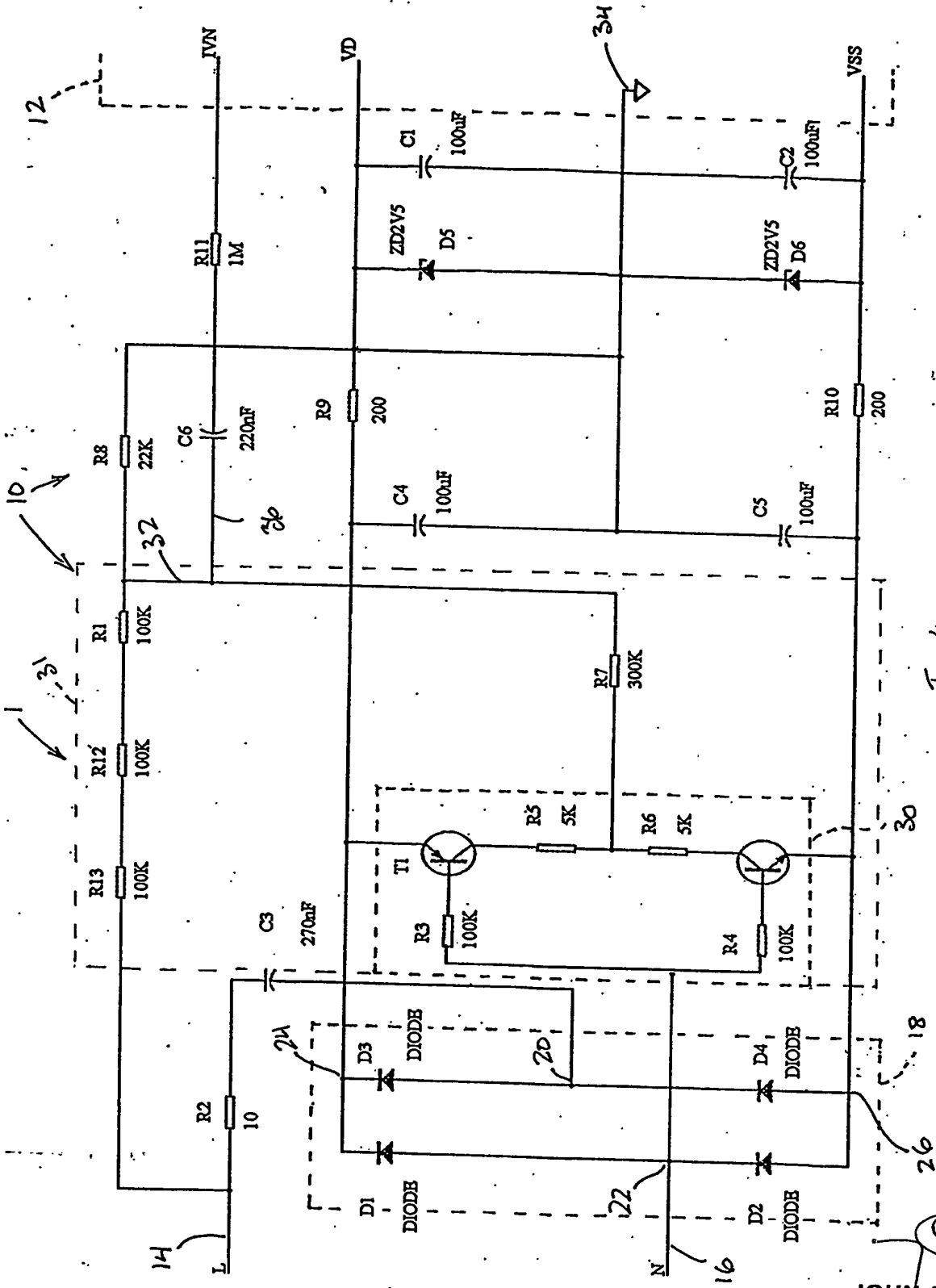
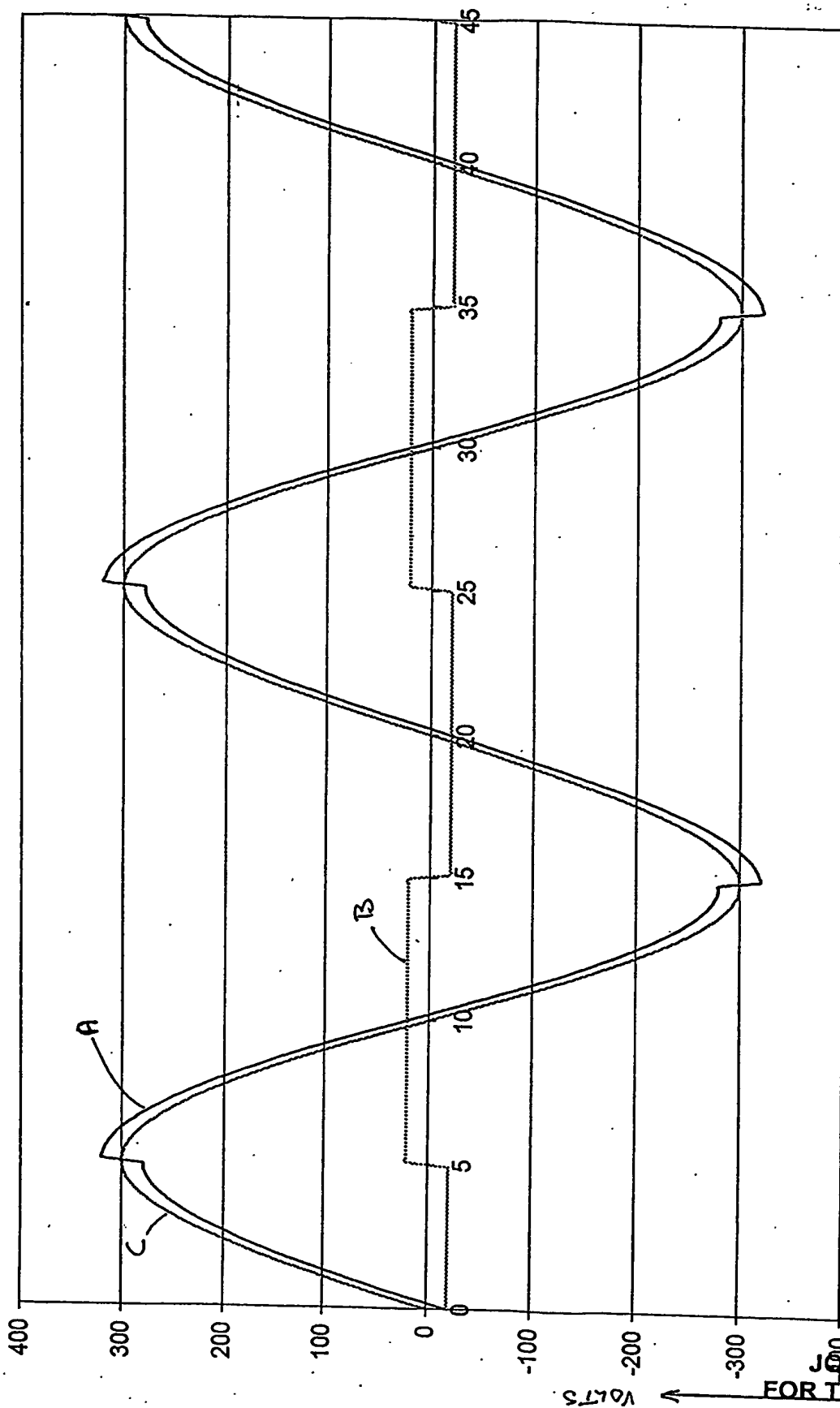


Fig 4

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